

SPECIFICATION AMENDMENTS

Amend the abstract of the disclosure that begins in line 2 on page 23 as follows:

First, second, and third semiconductor switches are connected in series between input and output terminals and first and second voltage application ~~means~~ circuits are connected in parallel to ~~the~~ first and third semiconductor switches, whereby providing a semiconductor switch circuit. Each voltage application ~~means is composed of a series-connected circuit of~~ circuit comprises a first or second voltage application semiconductor switch ~~and connected at the output side thereof with a first or second~~ direct current amplifier having a gain state of approximately +1 and whose input side is connected to the input or output terminal. One end of the first or second voltage application semiconductor switch is connected to ~~the~~ a first junction J of the first and second semiconductor switches, or to ~~the~~ a junction K of the second and third semiconductor switches, respectively. When first through third semiconductor switches are turned OFF, first and second voltage application semiconductor switches are turned ON to apply the ~~potential~~ potentials of input and output terminals to the first and second junctions J and K, respectively.